



**2005 IEEE International Conference on
MICROELECTRONIC SYSTEMS EDUCATION
"Educating Tomorrow's Microsystems Designers"**

12 - 13 June 2005 in Anaheim, California

Co-located with DAC 2005: 13 - 17 June 2005

<http://www.mseconference.org>

Paper Deadline: 15 Jan. 2005

The International Conference on Microelectronic Systems Education is dedicated to furthering undergraduate and graduate education in designing and building innovative microelectronic systems. The conference is held in the U.S. on odd years, and in Europe on even years, when it is called the European Workshop on Microelectronics Education.

MSE 2005 will focus on the following topics:

**Technologies: system-on-chip, mixed-signal, IP-based design,
Custom VLSI, ASIC and FPGA designs, Nanotechnology**

**Educational techniques: novel curricula, laboratories, distance learning,
Textbooks and student design projects**

**Industry involvement: experiences with university/industry partnership,
Preparing students for industry**

**Educational infrastructure: design and IP libraries, CAD tool access,
Internet-based tools, design evaluation tools, test and verification tools**

Confirmed invited speakers include:

**Reiner Hartenstein, Univ. Kaiserslautern
Anantha Chandrakasan, Massachusetts Institute of Technology
Mani Srivastava, Univ. of California, Los Angeles
Ralph Cavin, Semiconductor Research Corporation**

Authors must submit 2-page papers in IEEE double-column format.

General Chair:	John Lockwood, Washington Univ. St. Louis
Program Chair:	Mark Johnson, Purdue University
Proceedings Chair:	Andrzej Rucinski, Univ. of New Hampshire
Posters Chair:	Wayne Burleson, Univ. of Massachusetts